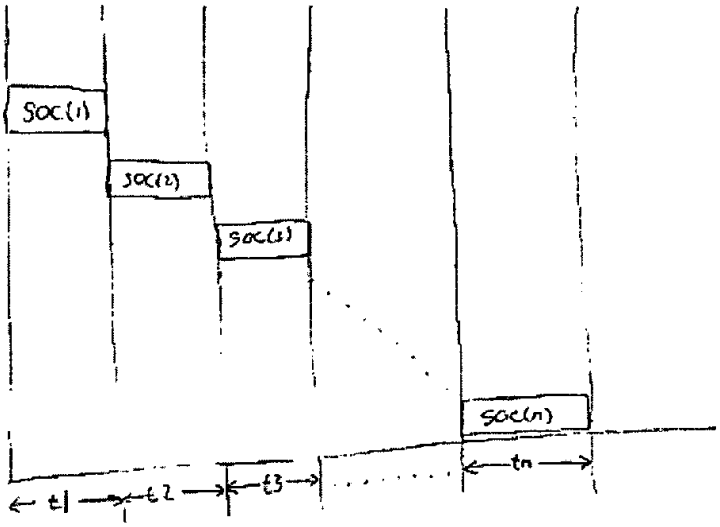

FIGURE 1A

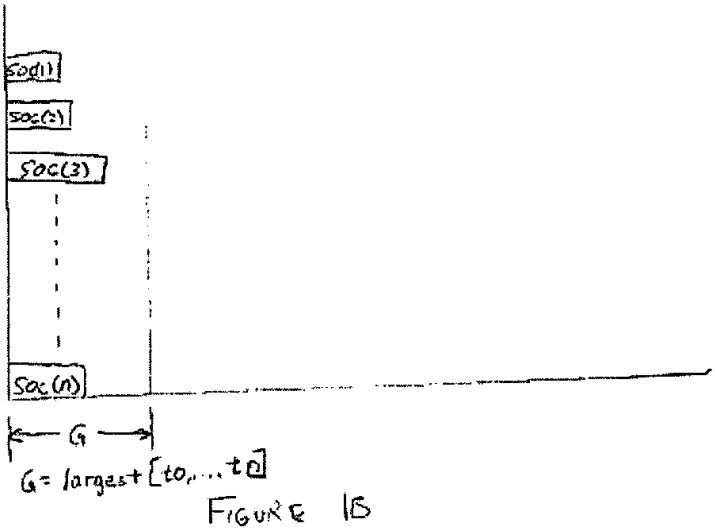

FIGURE 1B

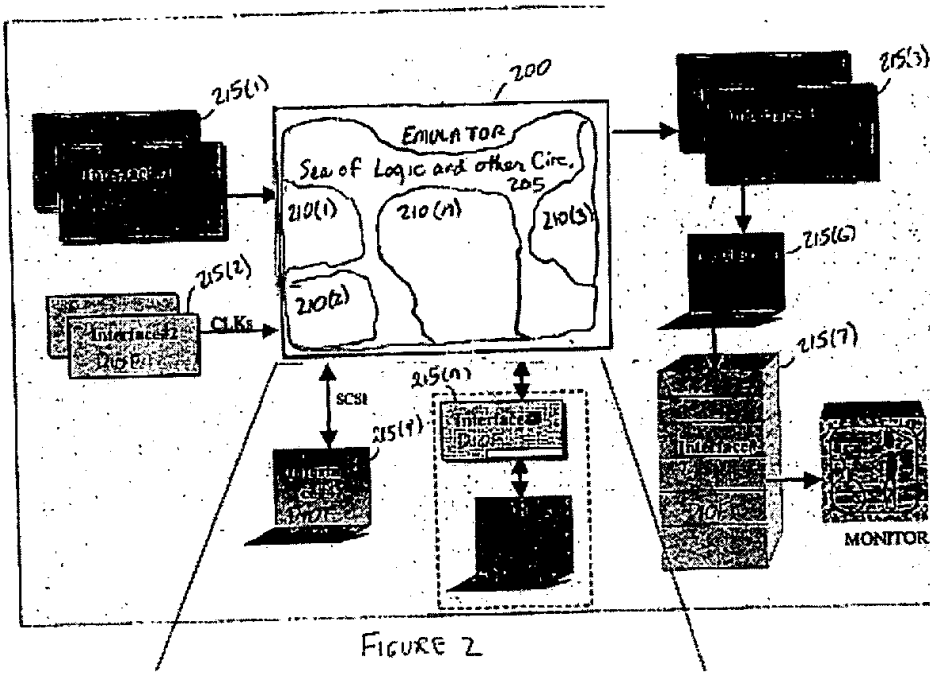
200
215(1)
EMULATOR
Sea of Logic and other Circ.
205
210(1)
210(n)
210(3)
210(2)
215(3)
215(2)
CLKs
215(6)
215(7)
SCSI
215(n)
215(4)
MONITOR

FIGURE 2

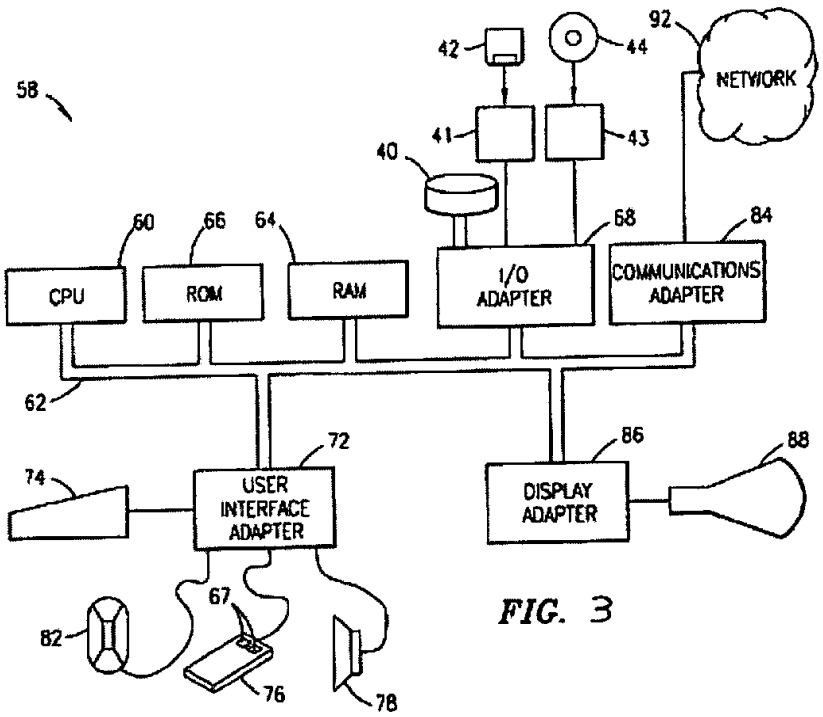
58
42
44
92
NETWORK
41
43
40
60
66
64
68
84
CPU
ROM
RAM
I/O ADAPTER
COMMUNICATIONS ADAPTER
62
72
86
88
74
USER INTERFACE ADAPTER
DISPLAY ADAPTER
67
82
76
78

FIG. 3

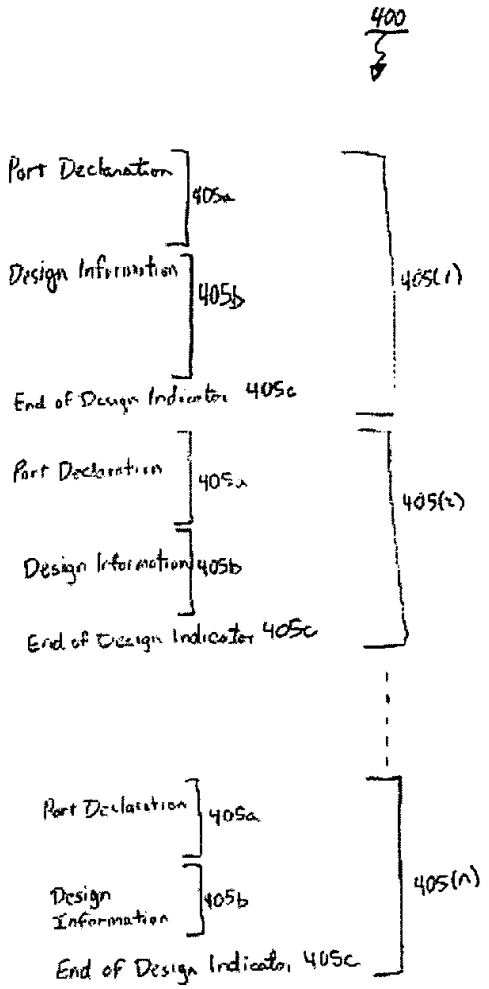
400
Port Declaration
405a
Design Information
405b
End of Design Indicator 405c
405(1)
Port Declaration
405a
Design Information
405b
End of Design Indicator 405c
405(2)
Port Declaration
405a
Design Information
405b
End of Design Indicator 405c
405(n)

FIGURE 4

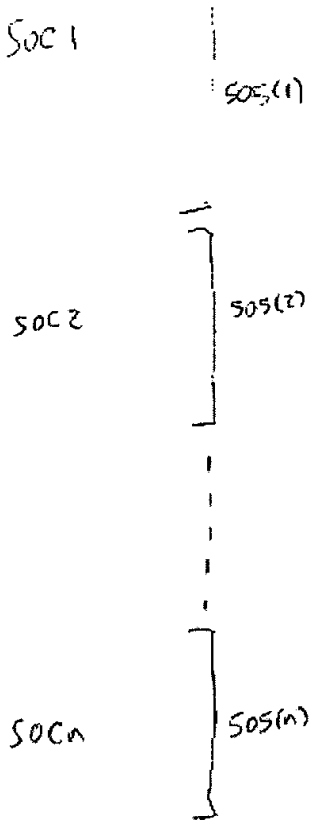

FIGURE 5